

07/31/03  
16310 U.S. PTO

# UTILITY APPLICATION

Attorney Docket No.: MICRON.104DV1  
First Named Inventor: Jigish D. Trivedi  
Title: TRANSISTOR GATE AND LOCAL INTERCONNECT  
Express Mail Label No.: EV 169567266 US

Direct all correspondence to Customer No.: 20995

Date: July 31, 2003

Page 1 of 2

## Mail Stop Patent Application

United States Patent and Trademark Office  
PO Box 1450  
Alexandria, VA 22313-1450

17858 U.S. PTO  
10/631921  
07/31/03

The following enclosures are transmitted herewith to be filed in the patent application of:

### Inventors:

1. Jigish D. Trivedi

## REQUEST AND CERTIFICATION UNDER 35 U.S.C. § 122(b)(2)(B)(i)

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. § 122(b).

### APPLICATION ELEMENTS:

(X) Specification in sixteen (16) pages

(X) Drawings in three (3) sheets

(X) Declaration by Inventors in one (1) pages

- Copy from parent application
- **Incorporation by Reference.** The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied, is considered a part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
- Examiner in prior application: Ahmed N. Sefer
- Group Art Unit of prior application: 2826

(X) Copy of Power of Attorney and Assignment from parent case

### CONTINUITY INFORMATION:

Application	Relationship	Parent App. No.	Filing Date	Status
This Application	is a Divisional of	09/360,703	07/26/1999	Allowed

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# UTILITY APPLICATION

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Page 2 of 2

- (X) **Incorporation by Reference.** The entire disclosure of the prior applications is considered a part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

## OTHER APPLICATION PARTS:

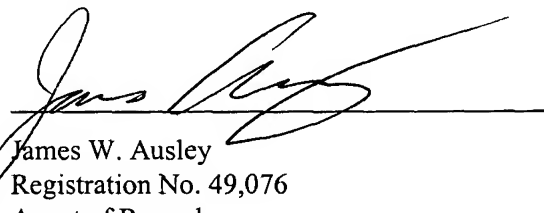
- (X) Information Disclosure Statement in one (1) page and PTO-1449 in two (2) pages listing twenty-eight (28) references. Listed references are not enclosed.
- (X) Return prepaid postcard.

## FILING FEES:

FEE CALCULATION				
FEE TYPE		FEE CODE	CALCULATION	TOTAL
Basic Utility		1001 (\$750)		\$750
Non-English Spec.		1053 (\$130)		\$0
Recordation Fee		8021 (\$40)	0 x 40 =	\$0
Excess Claims > 20	25 - 20 = 5	1202 (\$18)	5 x 18 =	\$90
Independent > 3	2 - 3 = 0	1201 (\$84)	0 x 84 =	\$0
Multiple Claim		1203 (\$280)		\$0
			<b>TOTAL FEE DUE</b>	<b>\$840</b>

- (X) A check in the amount of **\$840** to cover the Total Fee Due is enclosed.

The Commissioner is hereby authorized to charge any additional fees which may be required, now or in the future, or credit any overpayment to Account No. 11-1410.



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Commissioner for Patents  
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## CERTIFICATE OF MAILING BY "EXPRESS MAIL"

**Attorney Docket No.** : MICRON.104DV1  
**Applicant** : Jigish D. TRIVEDI  
**For** : TRANSISTOR GATE AND LOCAL  
INTERCONNECT  
**Agent** : James W. Ausley  
**"Express Mail"**  
**Mailing Label No.** : EV 169567266 US  
**Date of Deposit** : July 31, 2003

I hereby certify that the accompanying

- Transmittal Letter;
- Specification in sixteen (16) pages;
- Three (3) sheets of drawings;
- Copy of Declaration by Inventor from parent case;
- Copy of Assignment from parent case;
- Copy of Power of Attorney by Assignee from parent case;
- Information Disclosure Statement, PTO Form 1449 listing twenty-eight (28) references (refs. not enclosed);
- Check for Filing Fees(s);
- Return Prepaid Postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Nelson Merida

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